

UNITED STATES PATENT APPLICATION

of

Gary Dean Sasser

and

Chris Kiyoshi Togami

for

INTEGRATED OPTICAL TRANSCEIVER ARRAY

WORKMAN NYDEGGER
A PROFESSIONAL CORPORATION
ATTORNEYS AT LAW
1000 EAGLE GATE TOWER
60 EAST SOUTH TEMPLE
SALT LAKE CITY, UTAH 84111

INTEGRATED OPTICAL TRANSCEIVER ARRAY

CROSS-REFERENCE TO RELATED APPLICATIONS

[01] This application claims the benefit of U.S. Provisional Application No. 60/426,546, filed November 15, 2002, which is incorporated herein by reference in its entirety.

BACKGROUND OF THE INVENTION

1. The Field of the Invention

[02] The present invention generally relates to optoelectronic modules, including optical transceiver modules. More particularly, the present invention relates to an integrated optoelectronic module array system for minimizing the amount of space required in positioning such apparatus within an optical device.

2. The Related Technology

[03] Fiber optic technology is increasingly employed as a method by which information can be reliably transmitted via a communications network. Networks employing fiber optic technology are known as optical communications networks, and are marked by high bandwidth and reliable, high-speed data transmission.

[04] Optical communications networks employ optical transceivers in transmitting information via the network from a transmission node to a reception node. An optical transceiver at the transmission node receives an electrical signal (containing digital information or other data) from a network device, such as a computer, and converts the electrical signal via a laser and related components to a modulated optical signal. The

WORKMAN NYDEGGER
A PROFESSIONAL CORPORATION
ATTORNEYS AT LAW
1000 EAGLE GATE TOWER
60 EAST SOUTH TEMPLE
SALT LAKE CITY, UTAH 84111

optical signal can then be transmitted in a fiber optic cable via the optical communications network to a reception node of the network. Upon receipt by the reception node, the optical signal is fed to another optical transceiver that uses a photodetector and related components to convert the optical signal back into electrical signals. The electrical signals are then forwarded to a host device, such as a computer, for processing. The optical transceivers described above have both signal transmission and reception capabilities; thus, the transmitter portion of an optical transceiver can convert an incoming electrical signal into an optical signal, while the receiver portion of the transceiver can convert an incoming optical signal into an electrical signal.

[05] In a typical transceiver, the components responsible for transmitting and receiving optical signals are located in a transmitting optical sub assembly (“TOSA”) and a receiving optical sub assembly (“ROSA”), respectively. Specifically, the laser and associated components for producing an optical signal are located in the TOSA, while the photodetector and related components for receiving an optical signal are located in the ROSA. Optical signals are introduced to the ROSA and emitted from the TOSA via two respective optical ports defined in the optical transceiver module. The optical ports are typically arranged to be side-by-side when the bottom surface of the transceiver module is mounted within a device.

[06] Various optical devices employ a plurality of optical transceivers to perform the optical transmission and reception operations described above. For instance, optical devices such as switches and optical routers position a plurality of optical transceiver modules together within the device to enable several optical signal channels to be processed simultaneously. The plurality of optical transceivers are typically placed side-by-side along one or more edges of a host board within the device.

[07] An ever-present goal exists in the art to increase the number of optical ports available when implementing fiber optic systems. This goal extends to optical devices, such as optical switches, routers, and similar devices that utilize optical transceiver modules and other optoelectronic devices having such optical ports.

[08] Though optical transceiver design is largely governed by multi-source agreements (“MSAs”), such as the SFP and XFP MSA standards, which determine the physical dimensions of such modules, it has nonetheless been desirable in the past to reduce the amount of space occupied by the transceiver modules in order to increase optical port density. This is typically achieved in known designs by packing adjacent optical transceiver modules in close proximity to one another on the host board of the optical device and mounting the bottom surfaces of each module to the host board. However, this method alone results in only partial success in increasing optical port density.

[09] In a further effort to increase the optical port density of multiple optical transceiver modules, some known optical device designs mount the bottom surfaces of a first row of adjacent transceiver modules to an upper surface of the host board, and the bottom surfaces of a second row of adjacent transceivers to the host board lower surface. While this configuration can also improve transceiver module density, it nonetheless substantially improves neither inter-module spacing of adjacent transceiver modules nor corresponding optical port density.

[010] In light of the above discussion, a need exists for an optical transceiver module array that overcomes the above challenges. In particular, implementations and methods are sought by which optical transceiver modules can be positioned so as to maximize the density of optical ports of the transceiver modules within an optical device.

BRIEF SUMMARY OF THE INVENTION

[011] The present invention has been developed in response to the above and other needs in the art. Briefly summarized, embodiments of the present invention are directed to an integrated optoelectronic module array for use in optical devices and other environments. The integrated module array of the present invention is configured to increase the density of the optoelectronic modules, such as optical transceiver modules, within the optical device, thereby correspondingly increasing the density of optical ports per unit dimension within the optical device. This results in reduced space requirements within the optical device, and also enables the employment of relatively larger numbers of optical transceiver modules and corresponding optical ports per unit dimension within the optical device or other environment.

[012] In one embodiment, the present integrated module array is composed of various components, including a housing partially containing an integrated component structure, and a latching mechanism. The integrated component structure is composed of a plurality of optoelectronic sub-modules that are combined into a single structure. In one embodiment, the integrated component structure includes a plurality of optical transceiver sub-modules, each having dual optical ports. Though combined with other sub-modules within the integrated component structure, each optical transceiver sub-module operates independently in transmitting and receiving optical signals via its respective dual optical ports.

[013] The housing and integrated component structure of the integrated module array are in turn contained within an outer cage, which provides shielding from electromagnetic interference. The latching mechanism of the integrated module array selectively secures the array to the outer cage, which is correspondingly affixed to a host board. In turn, the host board can be contained within an optical device, such as an optical switch or router.

[014] The integration of the optical transceiver sub-modules into the integrated component structure maximizes the number of transceivers than can be included within the optical device. This in turn desirably corresponds to an increased density of optical ports for a given space. Multiple integrated module arrays can be included in an optical device, and the number of sub-modules that can be included in the sub-module structure of the integrated module array can vary according to system design and needs. Further, sub-modules of different types and purposes can be included in the same or adjacent integrated module arrays within an optical device, if desired.

[015] In sum, the configuration described above minimizes inter-module spacing of the optoelectronic devices utilized within the integrated module array. As a result, optical port density can be increased, which leads to greater signal transfer capacity, smaller optical device sizes, or both, in the case of optical transceiver components. Reduced EMI emissions from the various transceiver sub-modules that comprise the integrated array can also be realized by practice of the present invention, owing to the integrated physical nature of the array.

[016] In one embodiment, release sleeves are employed in connection with optical fiber connectors that pluggably interface with each sub-module of the integrated module array. This allows for ready insertion and removal of the connector from the respective sub-module despite the reduced spacing between adjacent sub-modules in the array.

[017] These and other features of the present invention will become more fully apparent from the following description and appended claims, or may be learned by the practice of the invention as set forth hereinafter.

BRIEF DESCRIPTION OF THE DRAWINGS

[018] To further clarify the above and other advantages and features of the present invention, a more particular description of the invention will be rendered by reference to specific embodiments thereof that are illustrated in the appended drawings. It is appreciated that these drawings depict only typical embodiments of the invention and are therefore not to be considered limiting of its scope. The invention will be described and explained with additional specificity and detail through the use of the accompanying drawings in which:

[019] Figure 1 is a perspective view of an integrated module array in accordance with one embodiment of the present invention;

[020] Figure 2 is a perspective view of dual integrated module arrays configured in accordance with another embodiment of the present invention;

[021] Figure 3A is a partial exploded view of one embodiment of the integrated module array, including a latching mechanism employed in connection with one embodiment of the present invention;

[022] Figure 3B is an assembled view of the integrated module array and latching mechanism of Figure 3A;

[023] Figure 4 is a perspective view of dual integrated module arrays having optical fibers connected thereto, in accordance with one embodiment of the present invention;

[024] Figure 5A is a perspective view of one embodiment of a release sleeve employed in connection with the present invention;

[025] Figure 5B is a top view of the release sleeve of Figure 5A;

[026] Figure 5C is a cross sectional side view of the release sleeve of Figure 5B taken along the lines 5C—5C;

[027] Figure 6A is a perspective view of an optical fiber connector having the release sleeve of Figures 5A-5C slidably connected thereto, wherein the release sleeve is in a disengaged position; and

[028] Figure 6B is a perspective view of the optical fiber connector and release sleeve of Figure 6A, wherein the release sleeve is in an engaged position.

WORKMAN NYDEGGER
A PROFESSIONAL CORPORATION
ATTORNEYS AT LAW
1000 EAGLE GATE TOWER
60 EAST SOUTH TEMPLE
SALT LAKE CITY, UTAH 84111

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[029] Reference will now be made to figures wherein like structures will be provided with like reference designations. It is understood that the drawings are diagrammatic and schematic representations of presently preferred embodiments of the invention, and are not limiting of the present invention nor are they necessarily drawn to scale.

[030] Figures 1-6B depict various features of embodiments of the present invention, which is generally directed to an integrated optoelectronic component array for use in optical devices, including optical switches, optical routers, and the like. The integrated optoelectronic component array of the present invention maximizes the placement of optoelectronic components, such as optical transceivers, within the optical device, thereby leading to a net increase in optical port density, in the case of optical transceivers. Though the discussion to follow will describe embodiments of the integrated optoelectronic component array as including optical transceiver components, the scope of this invention is not so limited. Consequently, while embodiments of the present integrated optoelectronic array can be configured to include optical transceiver components, the present invention can also be adapted for use with optoelectronic components of a variety of types.

[031] Reference is first made to Figure 1, which shows one embodiment of the present invention. In particular, Figure 1 shows an integrated optoelectronic component array (“integrated array”), generally designated at 10. The integrated array 10 is shown mounted to a host board 12. The host board 12 can be contained within an optical device (not shown), such as an optical switch, optical router, or the like. However, the present invention can be practiced in other environments as well.

[032] As shown in Figure 1, the integrated array 10 in one embodiment is composed of various components, including an integrated component structure 16, and a latching

mechanism 18. These components are received within an outer cage 14. In the present embodiment, the outer cage 14 is composed of a suitable material, such as a metallic material, that enables the cage to serve as an electromagnetic interference (“EMI”) shield for the integrated array 10. In addition, the outer cage 14 provides a covering for the integrated array 10 to protect interior portions thereof. Also, the outer cage 14 mounts to a surface of the host board 12 to provide a fixed location within which the integrated array 10 can be received. The size of the outer cage 14 can be scalable in different embodiments to accommodate component structures of different sizes, as will be discussed.

[033] The outer cage 14 includes a first end 14A, a second end 14B, and is sized to partially contain the component structure 16. In detail, Figure 1 shows the component structure 16 partially contained within the outer cage 14, wherein an optical interface portion 20 of the component structure 16 extends from the first end 14A of the outer cage 14. The latching mechanism 18 attaches to optical interface portion 20 and is utilized to selectively secure the integrated array 10 to the host board 12.

[034] The component structure 16 is composed of a plurality of optoelectronic components for use by the optical device. In one embodiment, the plurality of optoelectronic components is arranged into multiple sub-modules that are closely arranged in the component structure 16. This can be seen in Figure 1, wherein four optical transceiver sub-modules 30 are formed in the component structure. The transceiver sub-modules 30 can be integrated into a single structure, as shown in Figure 1. Alternatively, the transceiver sub-modules 30 can be separately formed then arranged together in a dense configuration in the component structure 16. A front portion of each transceiver sub-module 30 defines a portion of the optical interface portion 20 that extends from the first end 14A of the outer cage 14. Though the component structure 16 of the illustrated embodiment shows four sub-

modules, it is appreciated that the component structure 16 can be formed from two, three, six, eight, or more sub-modules.

[035] Each transceiver sub-module 30 in Figure 1 includes two optical ports 32 and 34 that are arranged in a top-over bottom configuration. Viewed together, the optical ports 32 and 34 of the four transceiver sub-modules 30 form a 2X4 optical port array along the face of the optical interface portion 20. It is conceivable, however, that optical port arrays of other sizes (i.e., 1x4, 2x6, 3x5, etc.) can also be configured. The optical ports 32 and 34 of each transceiver sub-module 30 in Figure 1 correspond to a transmission optical subassembly (“TOSA”) and a receiving optical subassembly (“ROSA”), respectively, that are contained within the component structure 16. In other embodiments, however, the optical ports can be arranged in other configurations and positions to correspond to alternative TOSA and ROSA configurations within the component structure.

[036] The component structure 16 is configured to minimize the spacing between the optical ports of the sub-modules included in the structure, thereby increasing optical port density in the integrated array 10 and, by extension, the optical device. This space minimization is partly accomplished in the present embodiment by integrating each sub-module 30 into the single component structure 16. This integration eliminates redundant structure between each transceiver. One redundancy that can be eliminated involves the outer walls that typically define known transceivers. In known optical transceiver grouping configurations, each transceiver is discrete from the other transceivers, and the transceivers are placed adjacent to one another in a side-by-side arrangement such that the outer walls of the transceivers are adjacent one another. Here, in contrast, each transceiver sub-module 30 of the component structure 16 shares at least one wall, such as the walls 36, with an adjacent sub-module in the component structure. In the present embodiment, each wall 36 is

integrally formed as a portion of the component structure 16 and can extend the length of the component structure to provide any necessary interior component separation and/or EMI shielding that can be necessary. Alternatively, the walls 36 can be shortened, lengthened, or shaped as may be desired to provide the proper structure and function of the integrated array 10.

[037] Additional space minimization is realized in the present embodiment of the integrated array 10 via the use of a single latching mechanism for the integrated array 10. In particular, the latching mechanism 18 enables the entire integrated array 10 to be selectively removed and replaced on the host board 12. The use of a single latching mechanism for the selective placement of multiple transceiver sub-modules eliminates the need for separate latching mechanisms for each transceiver, as in known transceiver implementations, thereby reducing the space requirements for the integrated array even further. This feature, along with the integration of each transceiver sub-module as discussed above can, in one embodiment of an integrated array having four transceiver sub-modules, result in space savings of about nine millimeters per integrated array. As mentioned, this also results in a relatively higher optical port density than what is possible in typical transceiver implementations.

[038] Note again that the inclusion of optical transceiver sub-modules in the integrated array is merely an example of the broader principles taught by the present invention. Indeed, one of a variety of optoelectronic components can alternatively be included in the integrated array in place of the transceiver sub-modules. Moreover, it is also possible to include optoelectronic components of different types within the same integrated array, if desired.

[039] A plurality of card portions 22 are shown extending from the second end 14B of the outer cage 14. Each card portion 22 extends from the component structure 16 and can include a card edge connector 23 thereon for electrically interfacing with one or more host boards 12 via a plurality of connector receptacles, one of which is shown mounted on the host board at 24. In turn, the host board 12 can be electrically connected to the host device. In this way, the component structure 16 and the components located therein, such as the TOSA and ROSA, can be electrically connected to the host device via the card edge connectors 23, the connector receptacle 24, and the host board 12.

[040] Reference is now made to Figure 2, which shows a perspective view of one embodiment of the present invention. In detail, Figure 2 shows two integrated arrays 10 positioned between corresponding host boards 12 in a “belly-to-belly” configuration, which depicts one of a variety of positional configurations that can be used in connection with this invention. As such, the upper integrated array 10 shown in Figure 2 has the same orientation as the integrated array shown in Figure 1, while the lower integrated array has an orientation that is rotated 180° about a lengthwise axis of the array from that shown in Figure 1. Note that the configuration shown in Figure 2 can be implemented within an optical device, such as an optical switch or router. As such, it is evident that multiple discrete integrated arrays can be combined for use within an optical device. Furthermore, each integrated array can include more or fewer sub-modules than what is shown in the accompanying drawings.

[041] Figure 2 affords another view of the card portions 22 that extend from the second end 14B of the outer cage 14. As partially shown in Figure 2, each integrated array 10 here includes four card portions 22 extending from the outer cage 14. The card portions 22 are portions of printed circuit boards (not shown) that are located within the component

structure 16. The printed circuit boards in one embodiment include the various components that comprise the TOSAs and ROSAs of each of the transceiver sub-modules 30, which are in turn in operable communication with the optical ports 32 and 34 of the respective sub-module 30. As such, each card portion 22 and card-edge connector 23 is indirectly and operably connected with the optical ports 32 and 34 of one or more of the transceiver sub-modules 30. In one embodiment, for instance, one card portion extends from the integrated array for each corresponding transceiver sub-module. As such, the TOSA and ROSA that pertain to that transceiver sub-module are operably connected to the host board 12 via the respective card portion and card-edge connector located thereon.

[042] The above discussion suggests various possible printed circuit board and card portion configurations. In the illustrated embodiment, two printed circuit boards (not shown) are contained in the component structure 16, with each printed circuit board having two card portions 22 that extend from the second end 14B of the outer cage 14 on the upper integrated array 10 depicted in Figure 2. As shown, two of these card portions 22 are received into connector receptacles 24 located on an upper host board 12, while the other two card portions are received by connector receptacles 24 positioned on a lower host board. The lower integrated array 10 has a similar configuration. However, in other embodiments other board and card portion combinations are possible such that fewer or more card portions extend from the integrated array 10. Additionally, connection schemes between the host board and the integrated array that are distinct from that explicitly shown and described herein are also possible.

[043] As mentioned, the integrated array of the present invention is scalable to accommodate different numbers of sub-module components. Though the embodiments shown in the accompanying figures depict the integrated array as including four sub-

modules, an integrated array can be configured to include two, three, six, or more sub-modules as may be needed for a particular application.

[044] Reference is now made to Figures 3A and 3B, which depict various additional features of the present invention. In detail, Figure 3A shows the integrated array 10 of Figure 1 in partial exploded view and rotated 180° about a lengthwise axis of the array. Also shown is the outer cage 14 already described above mounted to the host board 12. (For purposes of illustration an upper host board is not shown). As shown, the component structure 16 is attached to a housing 40 that covers interior components of each sub-module 30. The component structure 16 and housing 40 can be separately or integrally formed.

[045] The latching mechanism 18 of the integrated array 10 is shown in exploded view in Figure 3A. As mentioned, the latching mechanism 18 is employed to releasably secure the integrated array 10 within the outer cage 14, thereby enabling each transceiver to be selectively inserted into and removed from the outer cage array when desired.

[046] As seen in Figures 3A and 3B, the latching mechanism 18 is located on a surface of the optical interface portion 20 of the component structure 16. Figures 3A and 3B depict various components of the latching mechanism 18, including a bail 42 and a pivot block 44. The bail 42 in the illustrated embodiment is a shaped piece of rigid wire. The bail 42 has various segments, including a clasp 46, a first shoulder 50, a cam 52, and a second shoulder 54. The bail 42 is shaped as to allow it to operate as a user-actuated portion for inserting and removing the integrated array 10 from the outer cage 14.

[047] The first and second shoulders 50 and 54 of the bail 42 are received into corresponding retention slots 64 and 66 defined on the optical interface portion 20 of the component structure 16 such that the cam 52 is interposed between the retention slots and such that pivoting of the bail 42 about the retention slots can occur. The pivot block 44 is

positioned over the cam 52 of the bail 42, and is received into corresponding pivot points 68 and 70 defined on the optical interface portion 20. The pivot block 44 includes a lock pin 72 that movably extends through a hole 73 defined in the housing 40 for releasably engageing a similar hole 75 defined in the outer cage 14. A leaf spring 74, which extends from the housing 40, compressively engages the pivot block 44 to secure the pivot block while enabling it to pivot about the pivot points 68 and 70. In other embodiments, the leaf spring 74 can be defined on and extend from the optical interface portion 20 of the component structure 16.

[048] Figure 3B shows the latching mechanism 18 as assembled, forming part of the integrated array 10. As noted above, the latching mechanism 18 releasably secures the integrated array 10 in position within the outer cage 14. In detail, Figure 3B shows the latching mechanism 18 in a latched configuration, wherein the bail 42 is positioned such that the cam 52 thereof causes no pivoting of the pivot block 44 to occur. In this latched configuration, the leaf spring 74 biases the pivot block 44 such that the lock pin 72 extends through the hole 73 defined in the housing 40 and engages the hole 75 in the outer cage 14 when the integrated array is positioned within the outer cage. In other embodiments, however, the latching mechanism can be configured such that the lock pin engages a hole defined in the host board of the optical device in which the integrated array is located.

[049] To disengage the integrated array 10 from the outer cage 14, the bail 42 is pulled in an outward direction. This can be accomplished by a user grasping the clasp 46 and pulling away from the face of the optical interface portion 20 of the component structure 16. This in turn causes the bail 42 to swing out away from the face of the optical interface portion 20. This movement of the bail 42 causes the bail cam 52 to engage the pivot block 44 and pivot it such that the lock pin 72 disengages from the hole 75 in the outer cage 14. After

disengagement of the lock pin 72 from the outer cage hole 75, the integrated array 10 can be removed from the outer cage 14 by a pulling force from a user on the clasp 46.

[050] When it is desired to insert the integrated array 10 into the outer cage 14, a user can rotate the bail 42 toward the optical interface portion 20 by grasping the clasp 46 and pushing it in the desired direction. This action disengages the bail cam 52 from the pivot block 44, which readies the lock pin 72 for engagement with the outer cage hole 75. The integrated array 10 can then be inserted into its place within the outer cage 14, at which point the lock pin 72 can engage the hole 75 of the outer cage, thereby locking the integrated array in place. In this way, the integrated array 10 can be selectively and individually inserted into and removed from the optical device as desired. Further details regarding the latching mechanism described herein can be found in U. S. Patent No. 6,439,918 B1, entitled "Electronic Module Having an Integrated Latching Mechanism," filed October 4, 2001, which is incorporated herein by reference in its entirety. Notwithstanding the above discussion, however, it is appreciated that latching mechanisms having other structures and configurations can also be employed in embodiments of the integrated array. Moreover, a latching mechanism that is employed with the integrated array described herein can be located in locations other than that shown and described above.

[051] Reference is now made to Figure 4. Figure 4 shows another view of the belly-to-belly arrangement of two integrated arrays 10 that is also depicted in Figure 2. As already described in connection with Figure 2, the integrated array 10 is received within the outer cage 14, which in turn is mounted to one or more host boards 12. Each integrated array 10 includes four transceiver sub-modules 30 that are integrally arranged as part of the component structure 16. Figure 4 additionally shows first and second connectorized optical fibers sets 80 and 82 that are connected with the optical interface portion 20 of the two

integrated arrays 10, respectively. In detail, both the first and second connectorized optical fiber sets 80 and 82 are shown coupled with respective transceiver sub-modules 30 of each of the integrated arrays 10 such that first and second optical fibers 80A and 80B of the first optical fiber set are optically coupled with the optical ports 32 and 34 of the respective transceiver sub-module 30 on the lower integrated array in Figure 4, and such that first and second optical fibers 82A and 82B of the second optical fiber set are optically coupled with the optical ports 32 and 34 of the respective transceiver sub-module 30 on the upper integrated array. Though only two optical fiber sets are shown connected with the integrated arrays in Figure 4, it is appreciated that any number, including all of the transceiver sub-modules 30 of each integrated array of Figure 4 can have optical fiber sets connected thereto.

[052] Each of the first and second connectorized optical fiber sets 80 and 82 includes a standard LC duplex connector 104 that is fitted with a release sleeve 86 that facilitates engagement and disengagement of the optical fiber sets to and from respective transceiver sub-modules 30. Such engagement and disengagement can otherwise be difficult to achieve, given the reduced clearance between the optical ports 32 and 34 of adjacent transceiver sub-modules 30 in the integrated array 10. Thus, the release sleeves 86 facilitate ease of operation with regard to the array system 10.

[053] Reference is now made to Figures 5A-5C in describing various details regarding the release sleeve 86. In particular, the release sleeve 86 includes a substantially rectangular body 88 having a raised portion 90. In the illustrated embodiment, the raised portion is laterally more narrow than the body 88. The body 88 and raised portion 90 cooperate to define a first open end 92, while the body also defines a second open end 94. The bottom portion of the body 88 is also open. Opposing lateral sides of the body 88 include extended

portions 96 to assist in handling the release sleeve 86. Two lips 98 extend inwardly toward one another from a bottom edge 100 of the body 88 for engaging the LC duplex connector 104.

[054] As best seen in Figure 5C, the raised portion 90 defines a complexly curved top inner surface 102. This curved top inner surface 102 includes both convex and concave curve portions to form a cam surface that is used to selectively engage and disengage the LC duplex connector 104, as will be seen below.

[055] Figure 6A shows the release sleeve 86 slidably attached to the standard LC duplex connector 104, such as that used in Figure 4 to optically connect with the optical ports 32 and 34 of one of the transceiver sub-modules 30. The release sleeve 86 in Figure 6A is shown in a disengaged position that is used when the LC duplex connector 104 is fully coupled with one of the transceiver sub-modules of the integrated arrays 10. In such a coupled position, the LC duplex connector 104 is received by the optical interface portion 20 of the respective component structure 16 such that two connector tips 106 optically couple with the optical ports 32 and 34 of the transceiver sub-module 30. This causes two resilient clips 108 of the LC duplex connector 104 to engage with corresponding notches 110 (Figure 4) formed on the optical interface portion 20 of the respective component structure 16 to lock the LC duplex connector in a specified position.

[056] As stated, the release sleeve 86 is shown in Figure 6A in a disengaged position, wherein the release sleeve is slid along the LC duplex connector body in an axial direction away from connector tips 106. This position prevents substantial contact between the curved top inner surface 102 (Figure 5C) of the release sleeve raised portion 90 and a connector latch 112 of the LC duplex connector 104. Correspondingly, the connector latch 112 is shown disengaged from adjacent ends of each resilient clip 108. This configuration

ensures that a secure fit between the optical ports 32 and 34 of the respective transceiver sub-module 30 and the LC duplex connector 104 is achieved.

[057] In contrast, Figure 6B shows the release sleeve in an engaged position, which is used to retract the LC duplex connector 104 from engagement with the respective transceiver sub-module 30 of the integrated array 10. This position is achieved by sliding the release sleeve 86 along the LC duplex connector body in an axial direction toward the connector tips 106. This axial movement causes the curved top inner surface 102 of the release sleeve raised portion 90 to engage the connector latch 112 and force it into contact with each resilient clip 108 of the LC duplex connector 104. The resilient clips 108 in turn are forced downward to disengage each clip from the notches 110 (Figure 4) of the respective transceiver sub-module 30. At this point, the LC duplex connector 104 can be removed from engagement with the respective transceiver sub-module 30. Thus, it is seen that the release sleeve enables insertion and removal of the LC duplex connector by virtue of its axial position with respect to the LC duplex connector, thereby obviating the need for direct manual manipulation of the LC duplex connector. Each transceiver sub-module of the array system can therefore be connected with an LC or other similar optical fiber connector via respective release sleeves as shown herein. Of course, the release sleeve can be adapted to operate with single or duplex connectors.

[058] The release sleeve can be composed of any suitable material that allows it to function as described above. In one embodiment, the release sleeve is composed of injection molded thermoplastic. In another embodiment, the thermoplastic of the release sleeve further includes a specified percentage of glass fibers intermixed therewith. Alternatively, a variety of other resins can be used.

[059] In addition to the above description, it is appreciated that the release sleeve can be adapted to operate with optical fiber connectors having physical characteristics that vary from that shown here. Moreover, the release sleeve can include designs or features in addition or alternative to those described herein.

[060] The present invention may be embodied in other specific forms without departing from its spirit or essential characteristics. The described embodiments are to be considered in all respects only as illustrative, not restrictive. The scope of the invention is, therefore, indicated by the appended claims rather than by the foregoing description. All changes that come within the meaning and range of equivalency of the claims are to be embraced within their scope.

What is claimed is:

WORKMAN NYDEGGER
A PROFESSIONAL CORPORATION
ATTORNEYS AT LAW
1000 EAGLE GATE TOWER
60 EAST SOUTH TEMPLE
SALT LAKE CITY, UTAH 84111